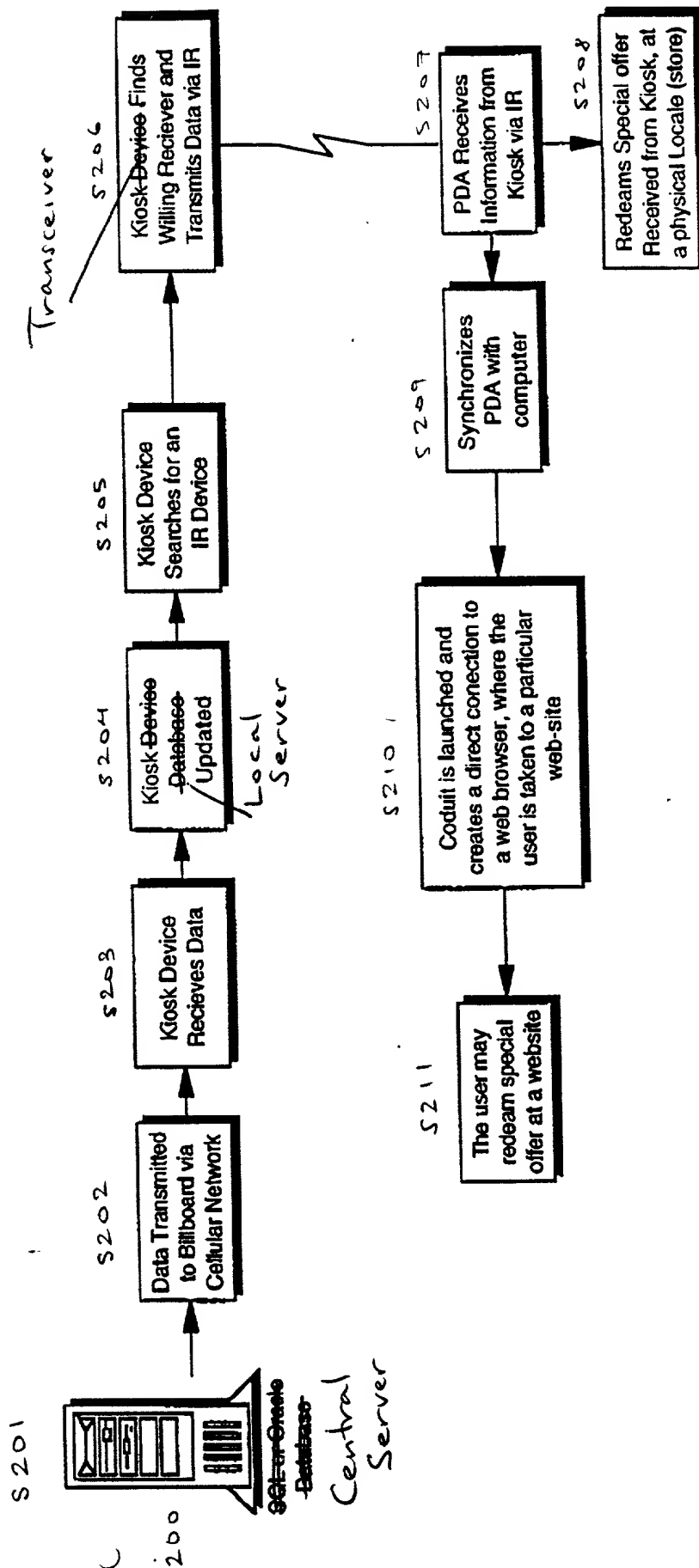


FIG. 1



F16.2

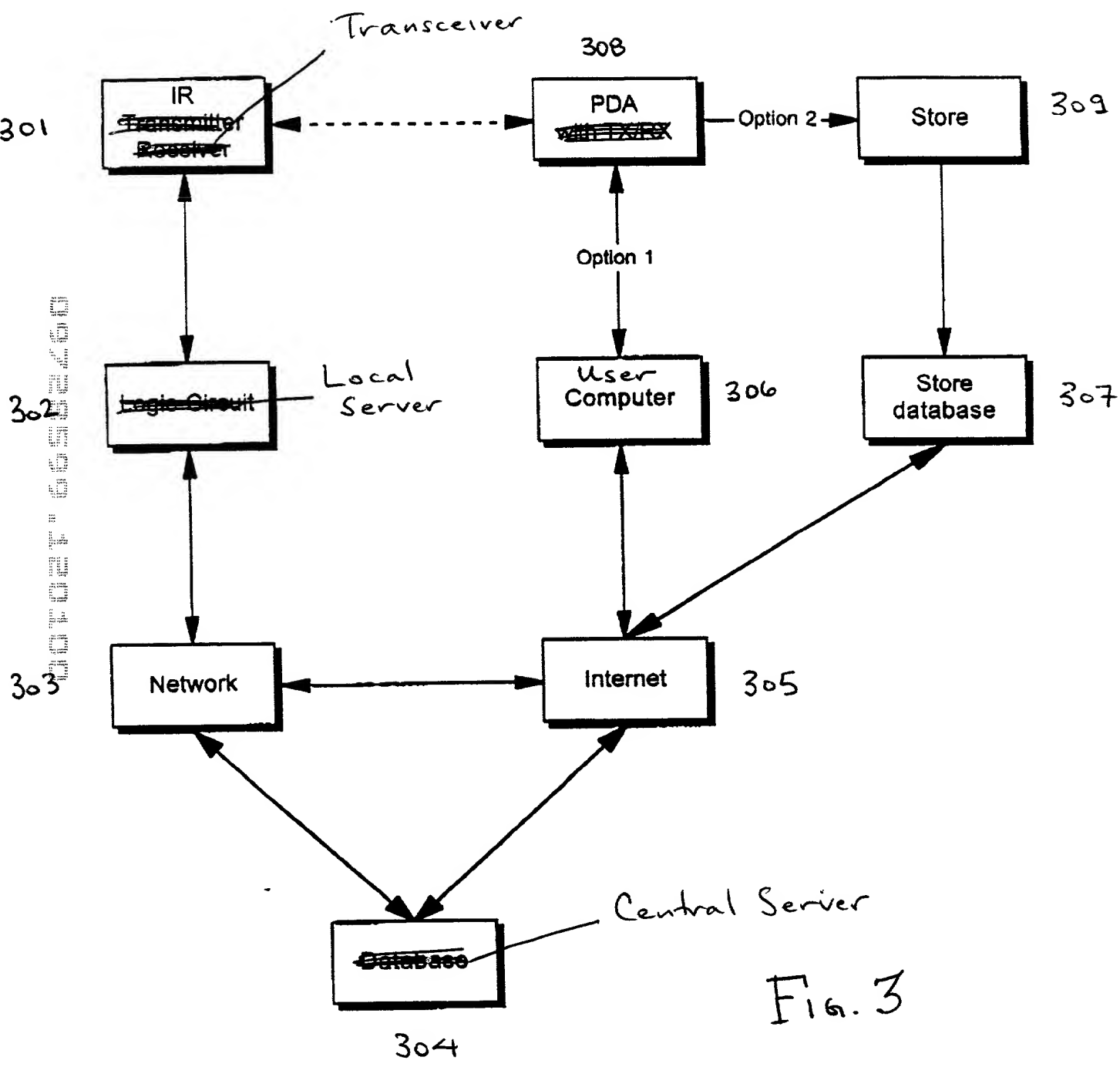


Fig. 3

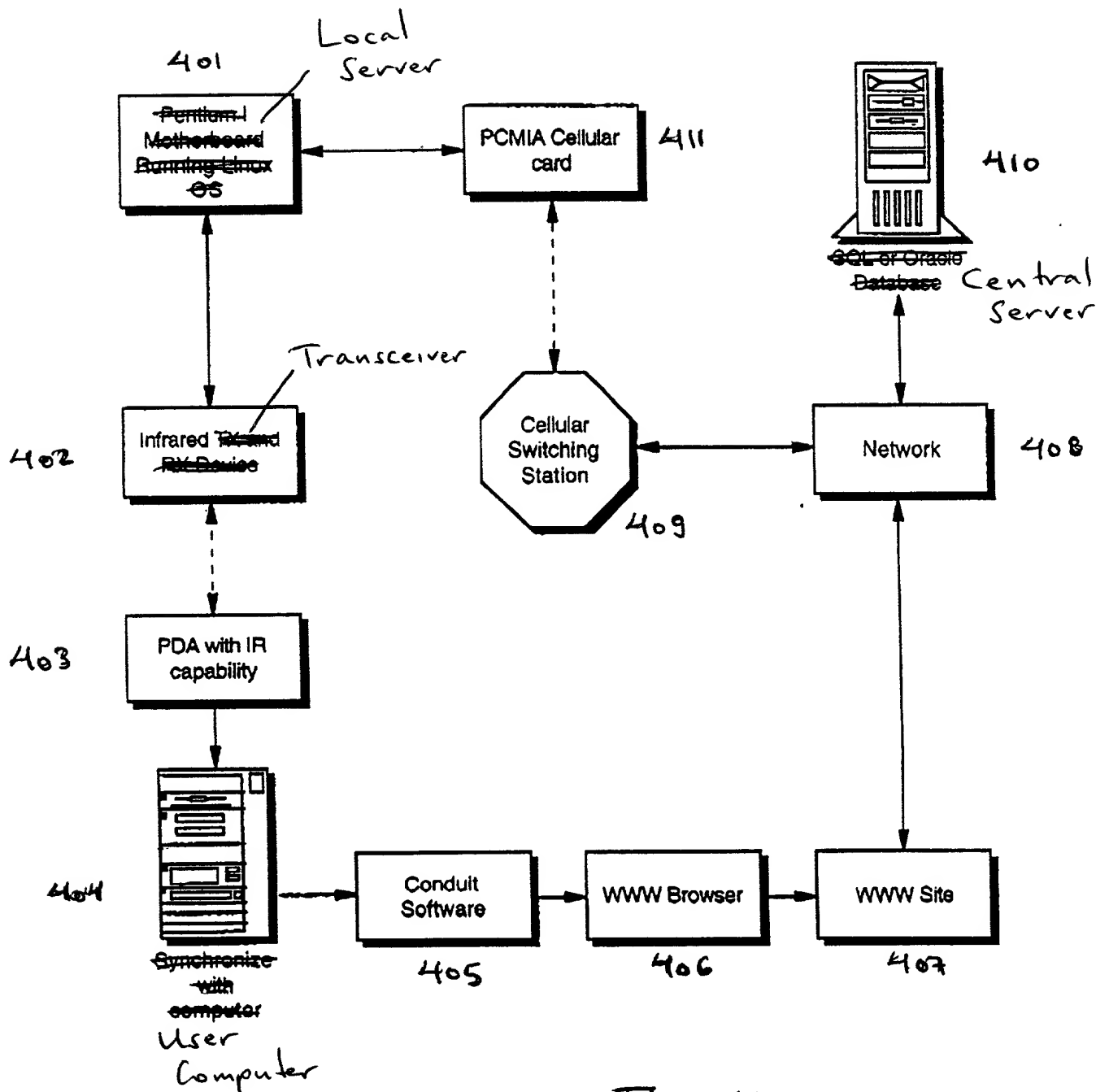
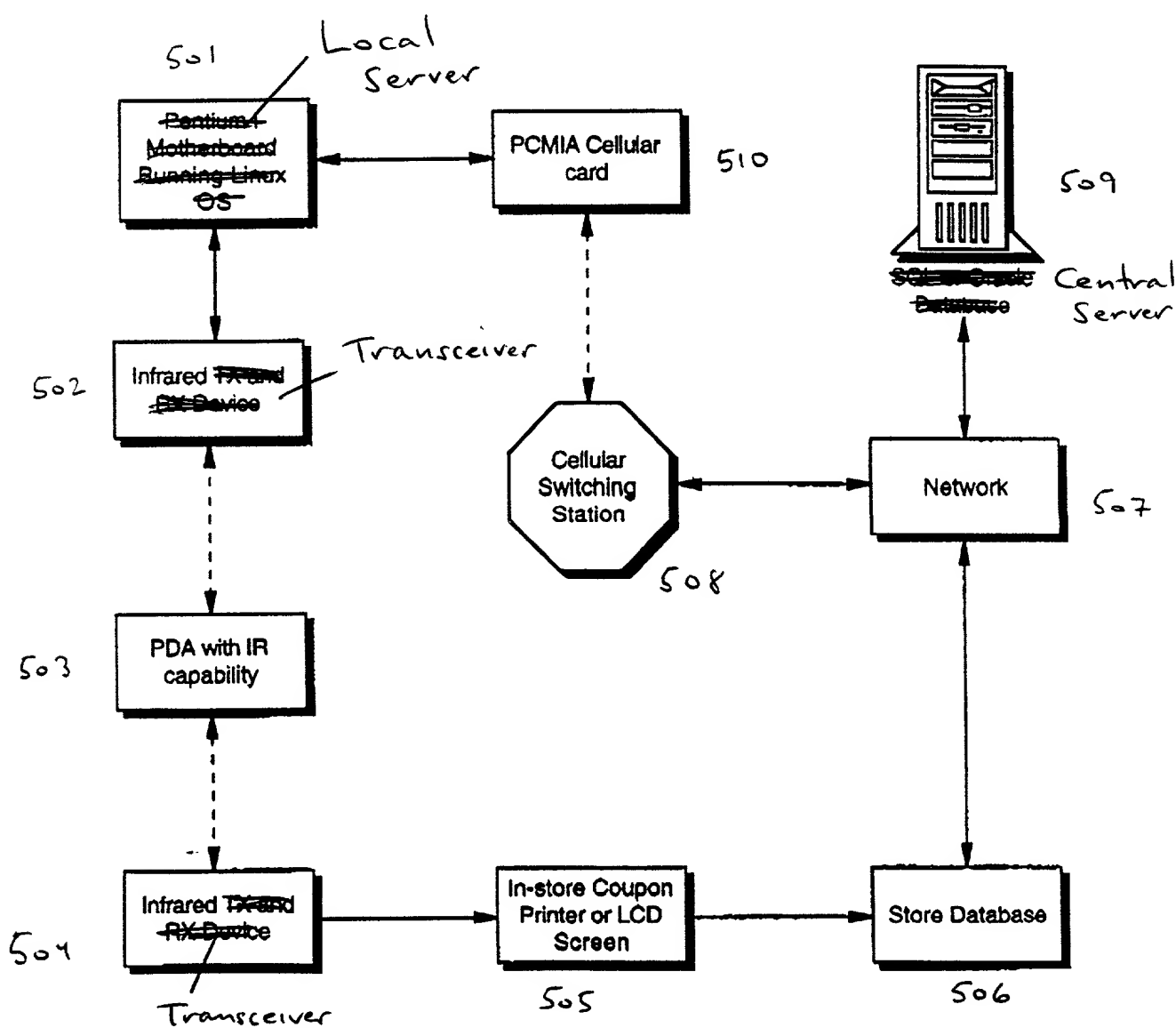


FIG. 4



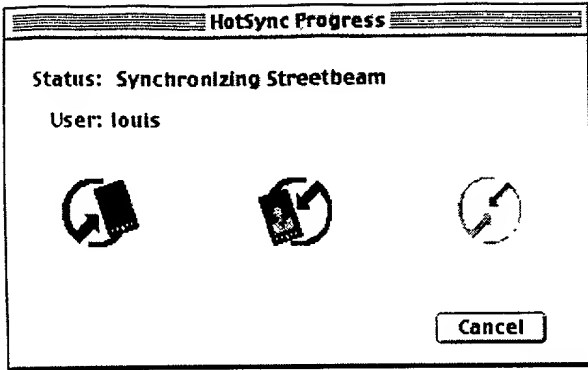


Fig. 6

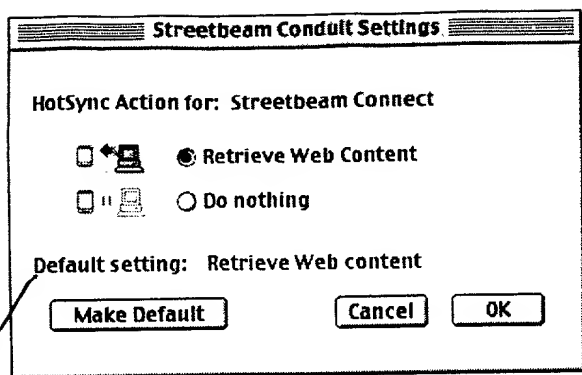


FIG. 7

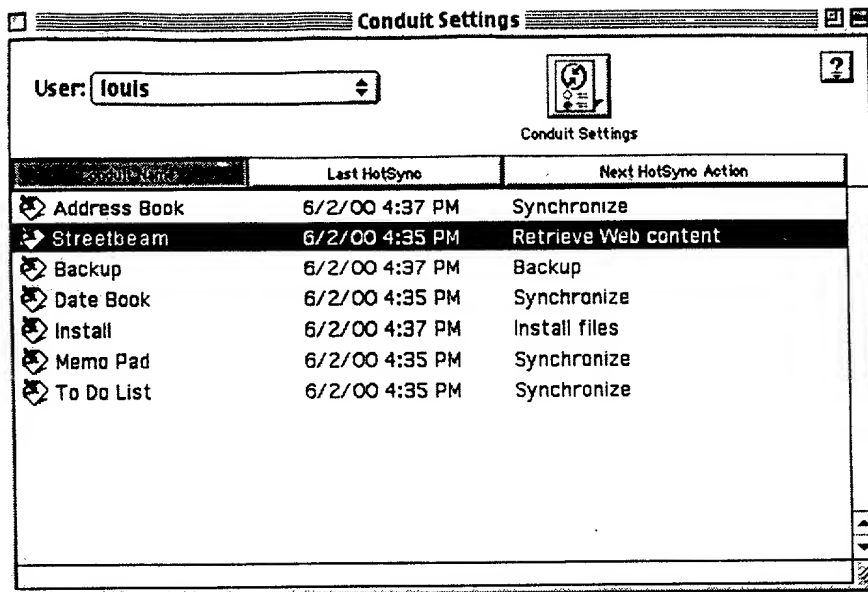
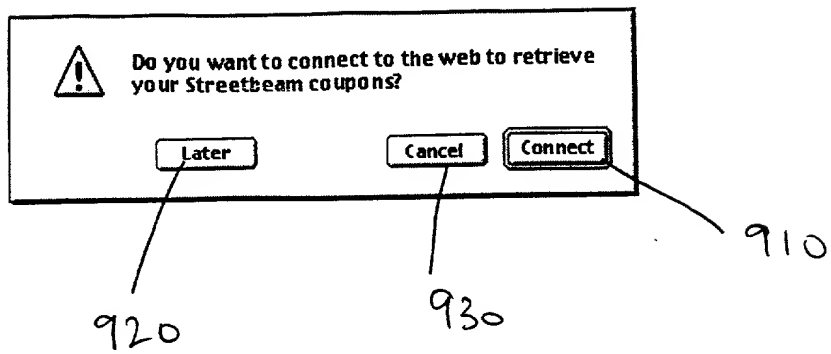


FIG. 8



#16. 9

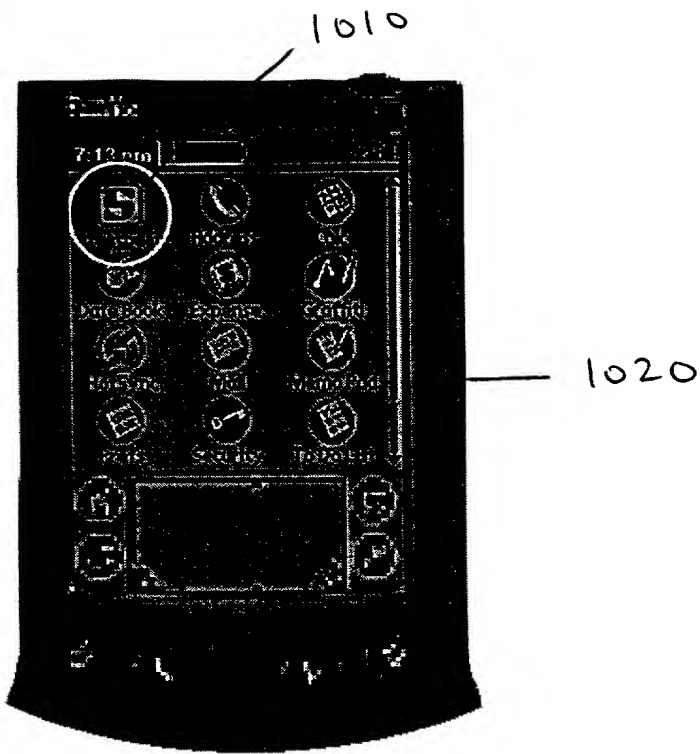


Fig. 10

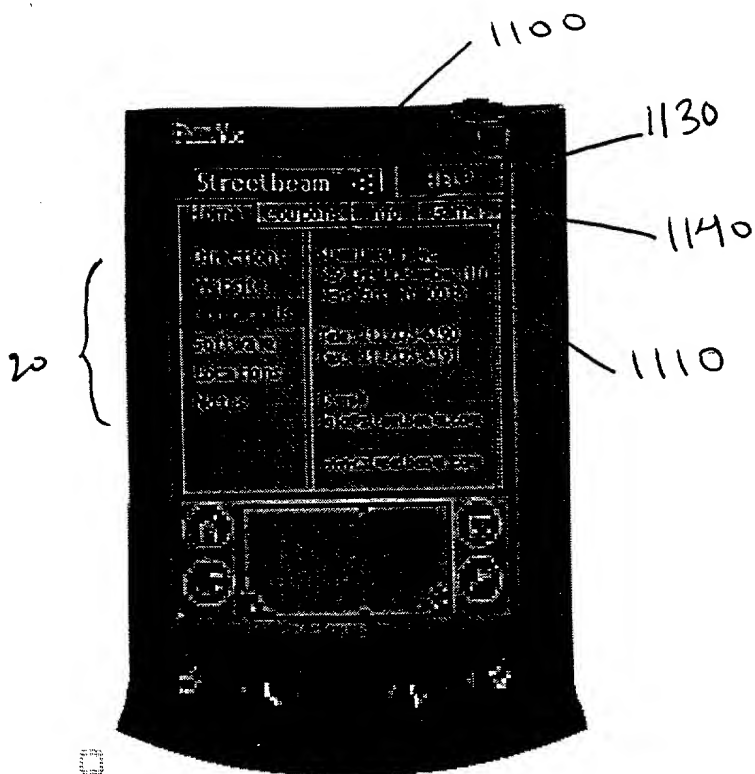


FIG. 11

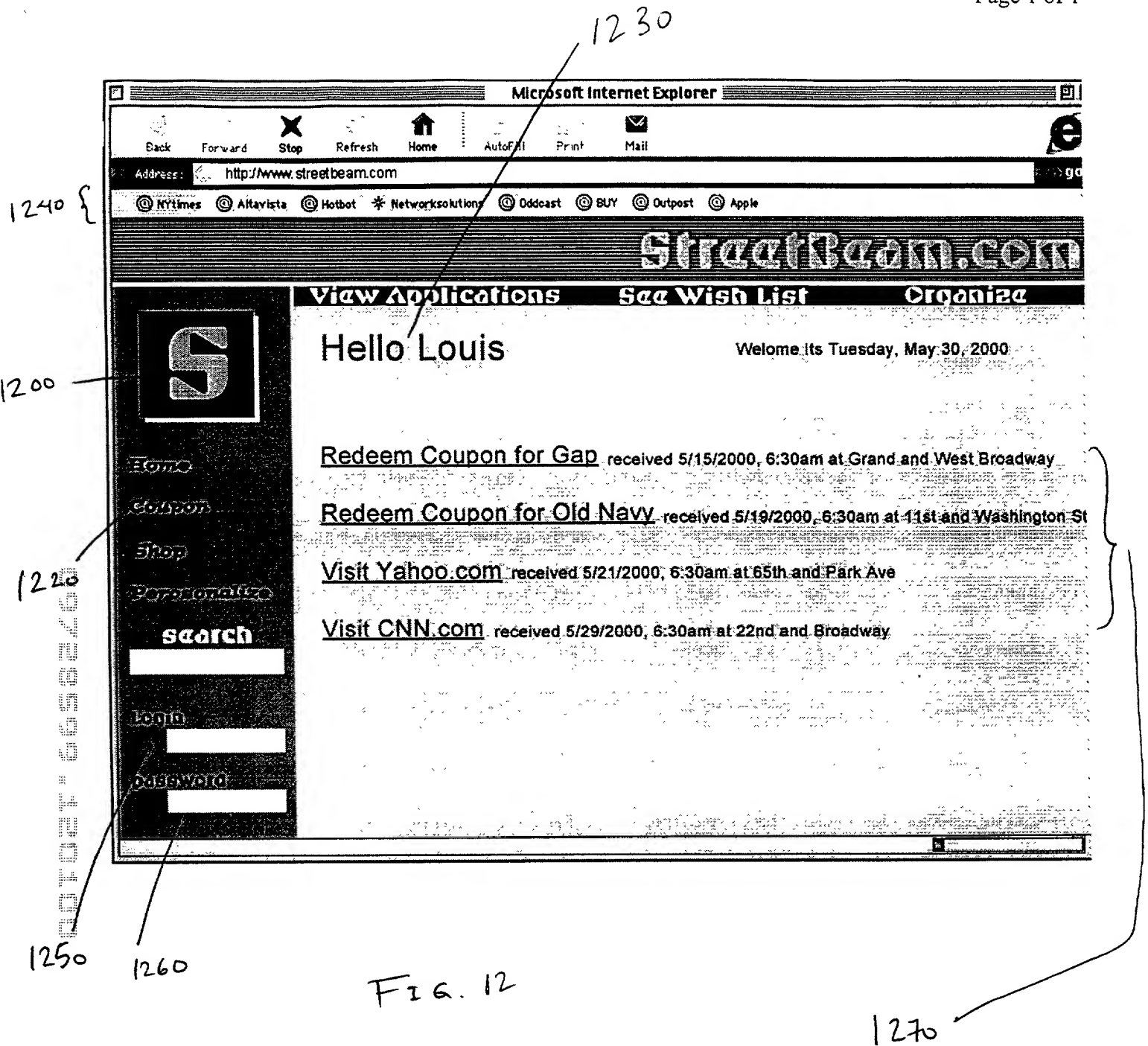


FIG. 12

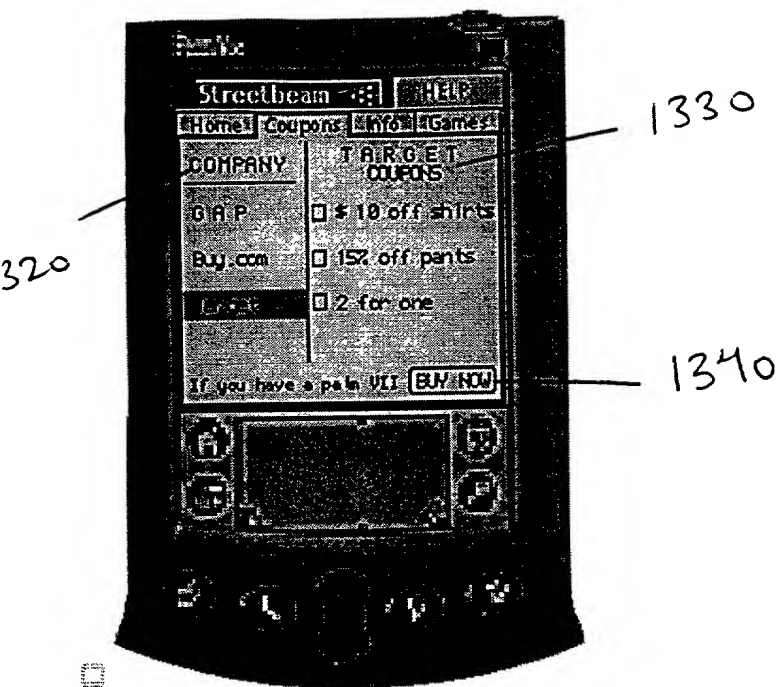


FIG. 13

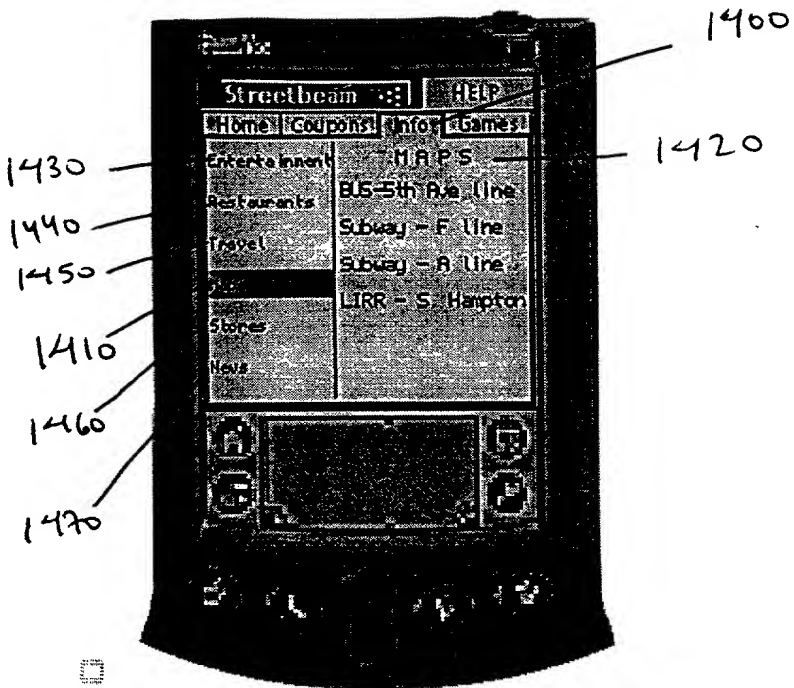


Fig. 14

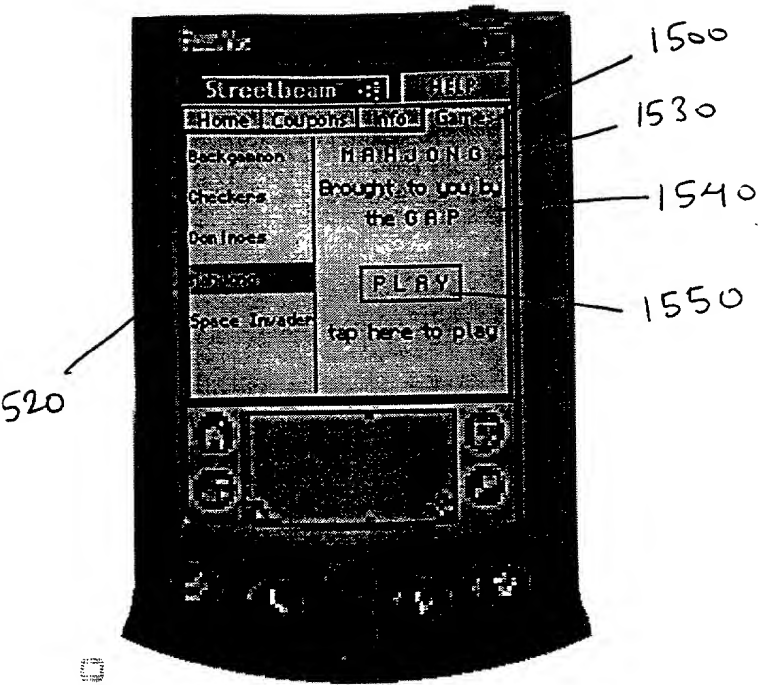


FIG. 15

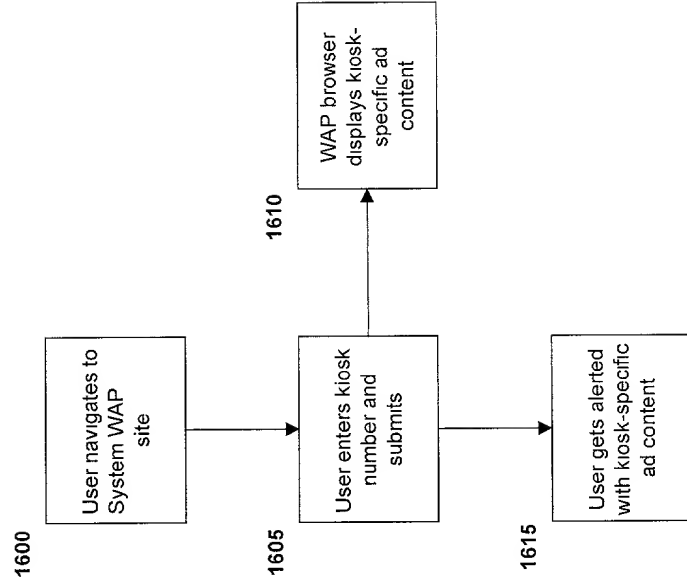
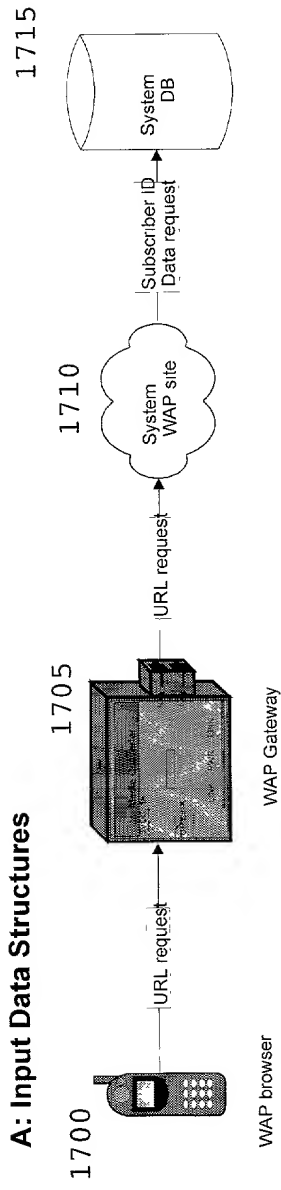


FIGURE 16

A: Input Data Structures



B: Output Data Structures

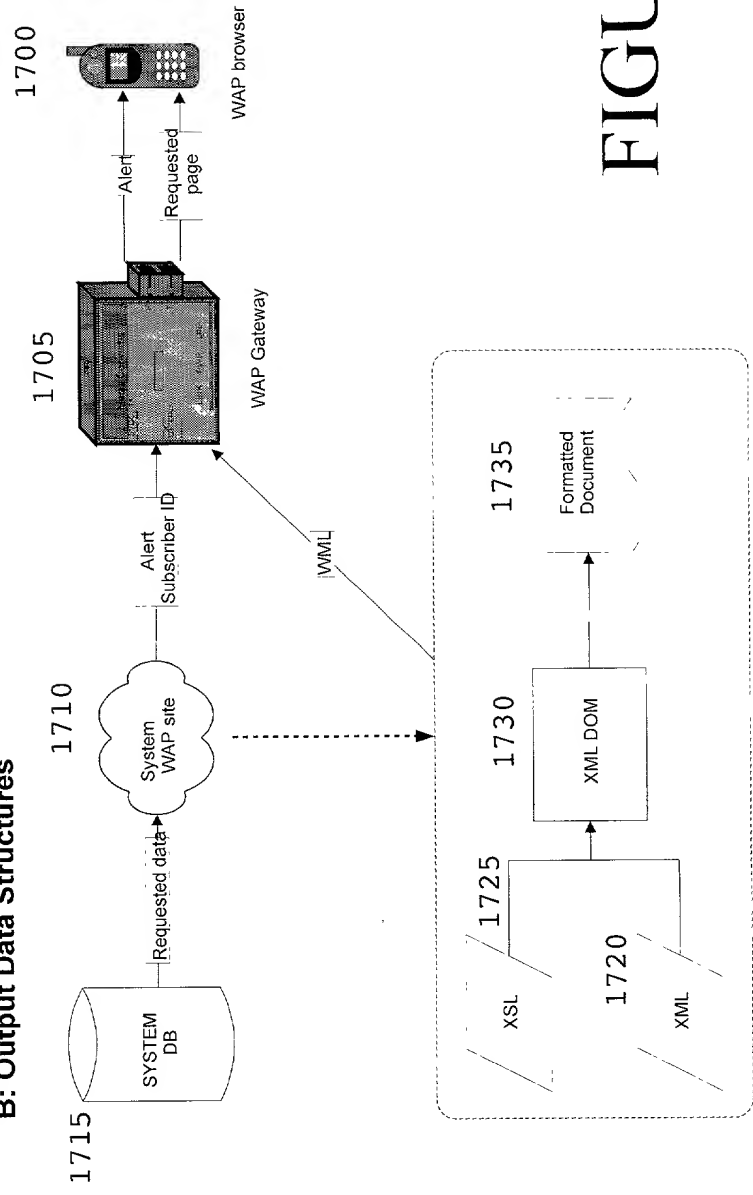


FIGURE 17

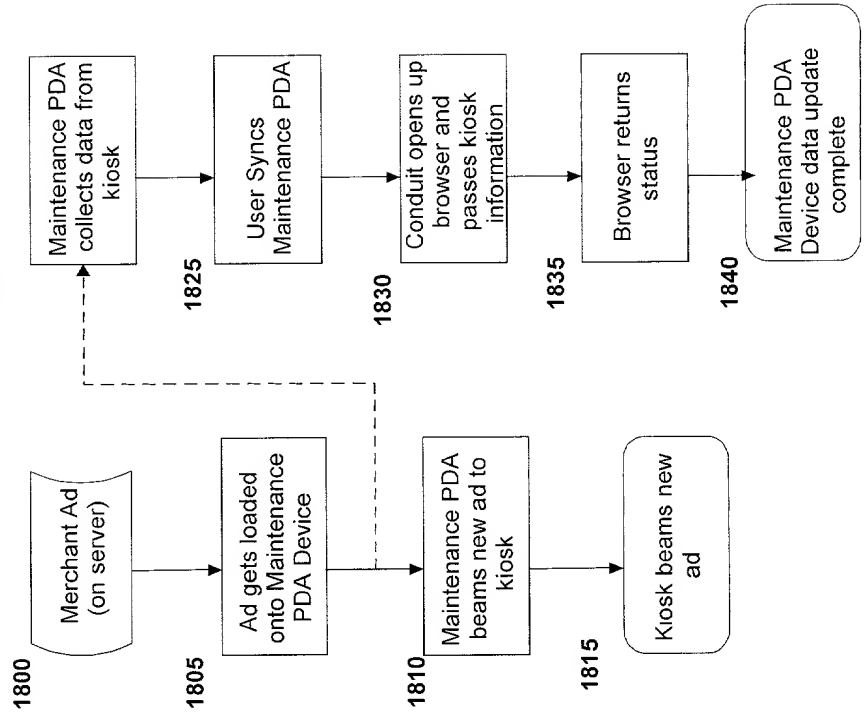


FIGURE 18

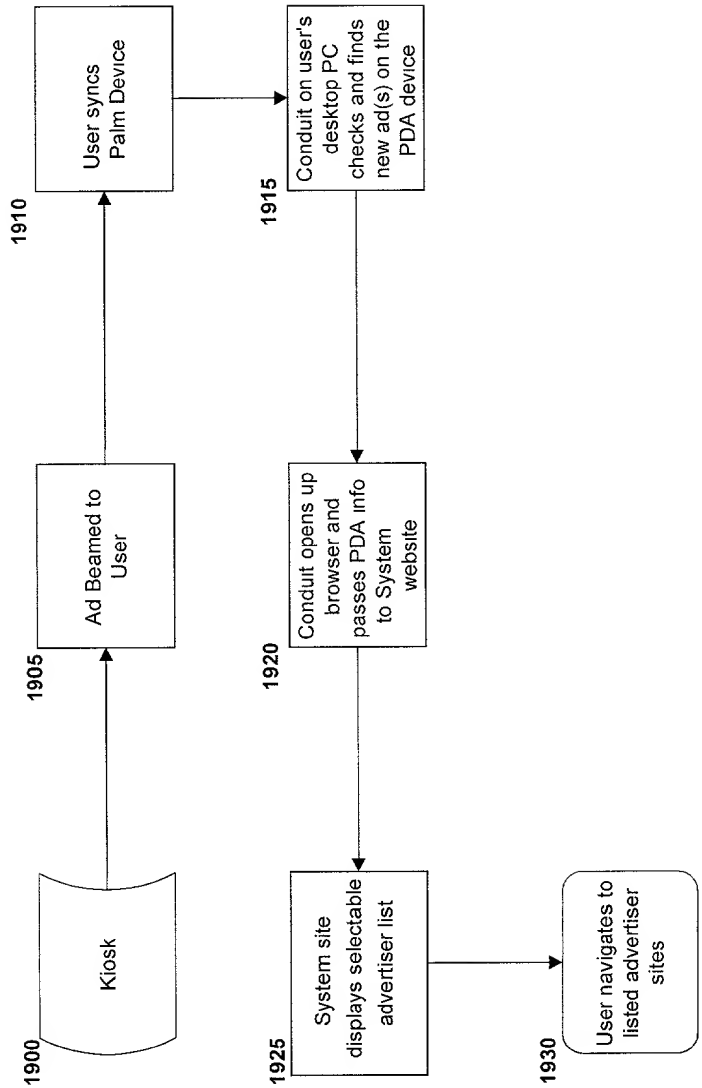


FIGURE 19

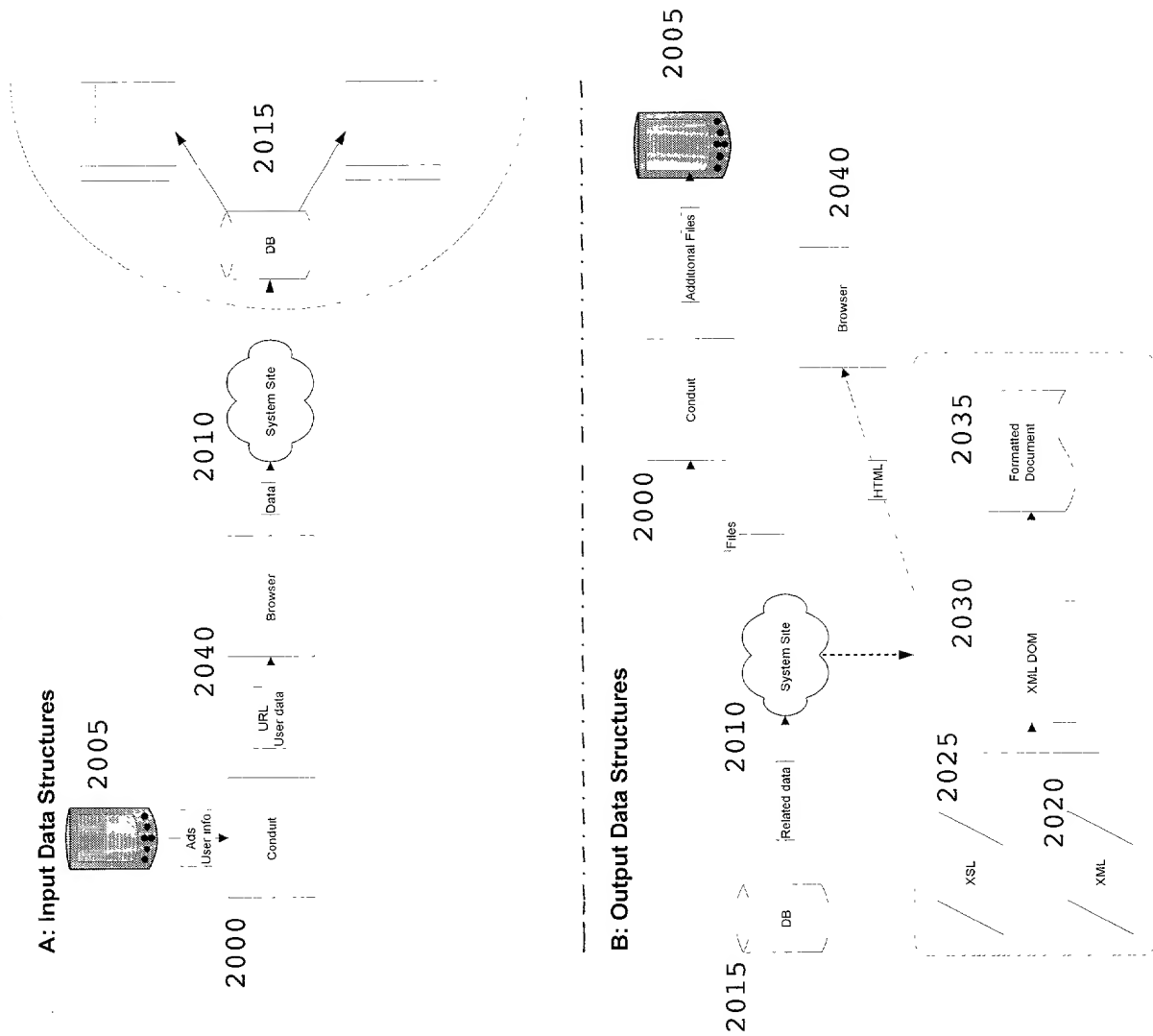


FIGURE 20

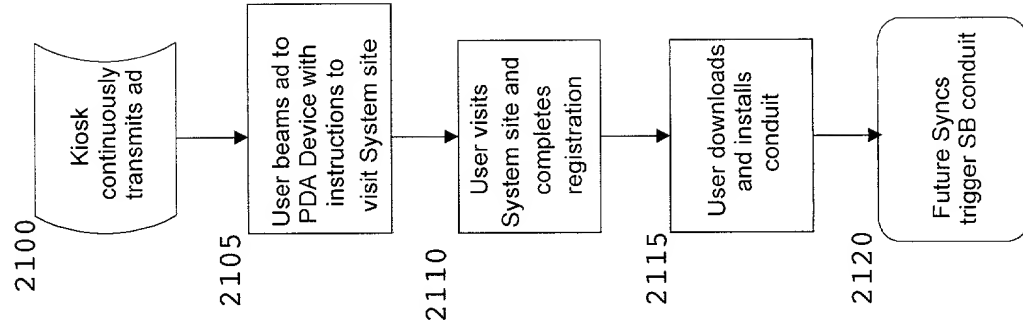
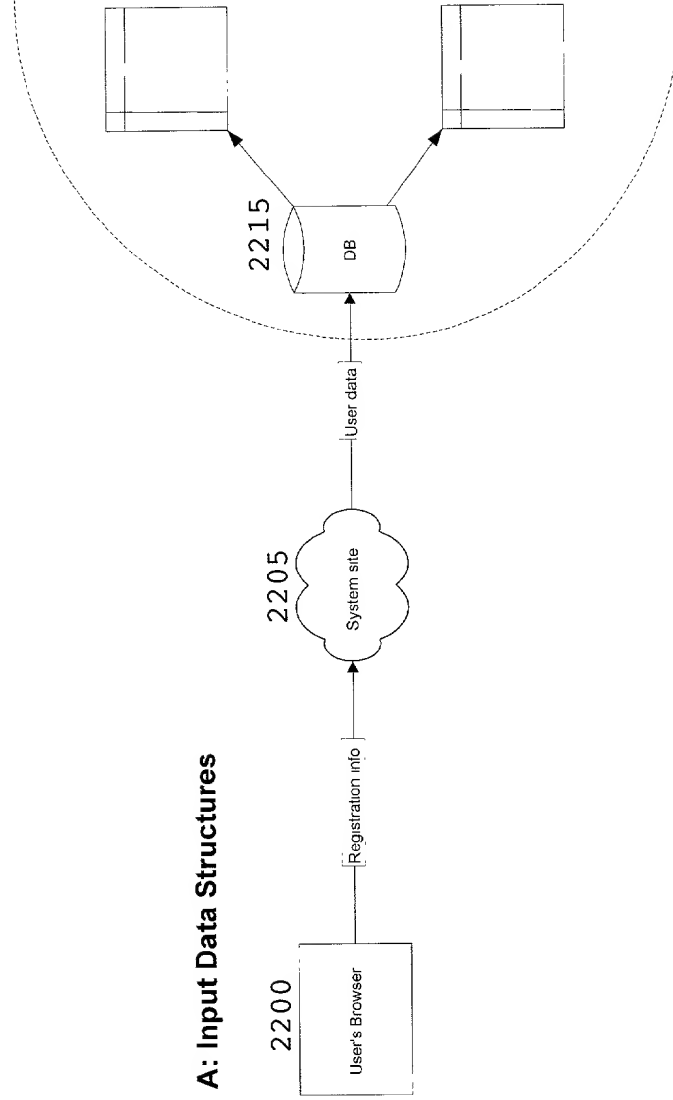


FIGURE 21

FIGURE 22

A: Input Data Structures



B: Output Data Structures

